AMENDMENTS TO THE CLAIMS:

1. (currently amended) A re-configurable interface used in modular electronic architectures comprising:

a host;

at least one module for interfacing with the host to provide additional functionality to the host; ~~and~~

a configuration controller located in the host for reading at least one memory device located in the at least one module for providing configuration information to the host;

a configurable logic operating with the configuration controller for configuring a host interface to operate with an at least one module interface; and

a plurality of connector pins at the host and at least one module that are electrically configured using the configurable logic.

2. (original) A re-configurable interface as in claim 1, wherein the configuration controller self-extracts the configuration information from the at least one memory device after interfacing with the at least one module with the host.

3. (Original) A re-configurable interface as in claim 1, further including a dedicated serial interface for exchanging information from the at least one memory device to the configuration controller.

4. cancel

5. cancel

6. (Original) A re-configurable interface as in claim 1, further including a microprocessor for communicating with the configuration controller.

7. (currently amended) A re-configurable electronic interface system for providing communication between a primary host device and a secondary accessory device comprising:

an interface controller including re-configurable logic associated with the primary host device;

at least one memory device associated with the secondary accessory device for communicating configuration information to the interface controller;

<u>a plurality of electrical interface pins for connecting the primary host device and the secondary accessory device;</u> and

wherein the interface controller sets the re-configurable logic of the <u>re-configurable</u> electronic interface <u>system</u> based on information from the at least one memory device for allowing compatibility between the secondary accessory device and the primary host device <u>by</u> <u>configuring the plurality of electrical interface pins according to the re-configurable logic</u>.

8. cancel

9. (Original) A re-configurable electronic interface system as in claim 7, wherein the interface controller self-extracts the configuration information from the at least one memory device.

10. (Original) A re-configurable electronic interface system as in claim 7, wherein the interface controller and at least one memory device communicate via a dedicated serial interface.

11. (currently amended) A re-configurable electronic interface system as in claim 7, wherein the interface controller ~~communications~~ <u>communicates</u> with a microprocessor.

12. (currently amended) method of reconfiguring an interface used in a modular electronic architecture between a primary host device and a secondary module device comprising the steps of:

providing an interface controller associated with the primary host device;

reading configuration information from at least one memory associated with the secondary module device; ~~and~~

setting configurable logic in the primary host device such that an electrical interface is established between the primary host device and the secondary module device; and

configuring a plurality of pins in the interface to provide capability between the primary host device and the secondary module device.

13. cancel

14. (Original) A method of reconfiguring an interface as in claim 12, further comprising the step of:

self-extracting the configuration information from the at least one memory when the secondary module device is connected with the primary host device.

15. (Original) A method of reconfiguring an interface as in claim 12, further comprising the step of:

communicating between the interface controller and the at least one memory via a dedicated serial interface.

16. (Original) A method of reconfiguring an interface as in claim 12, further comprising the step of:

controlling the interface controller using a microprocessor.